

FIG. 1. PRIOR ART

2/19

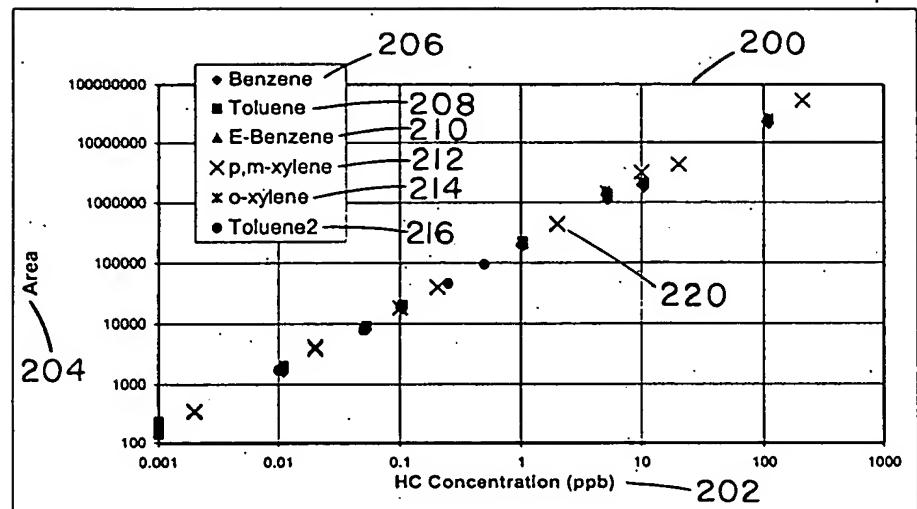


FIG. 2 PRIOR ART

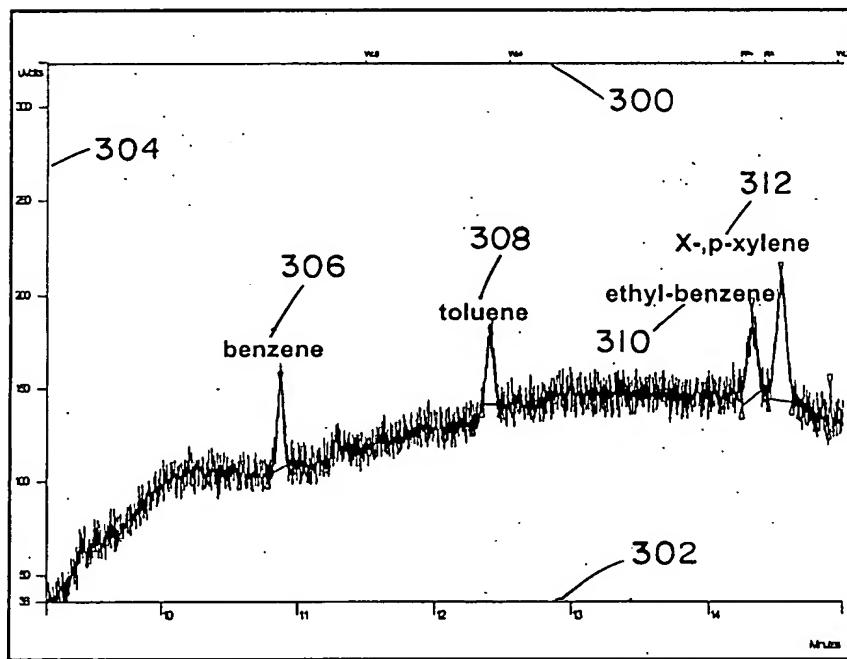


FIG. 3 PRIOR ART

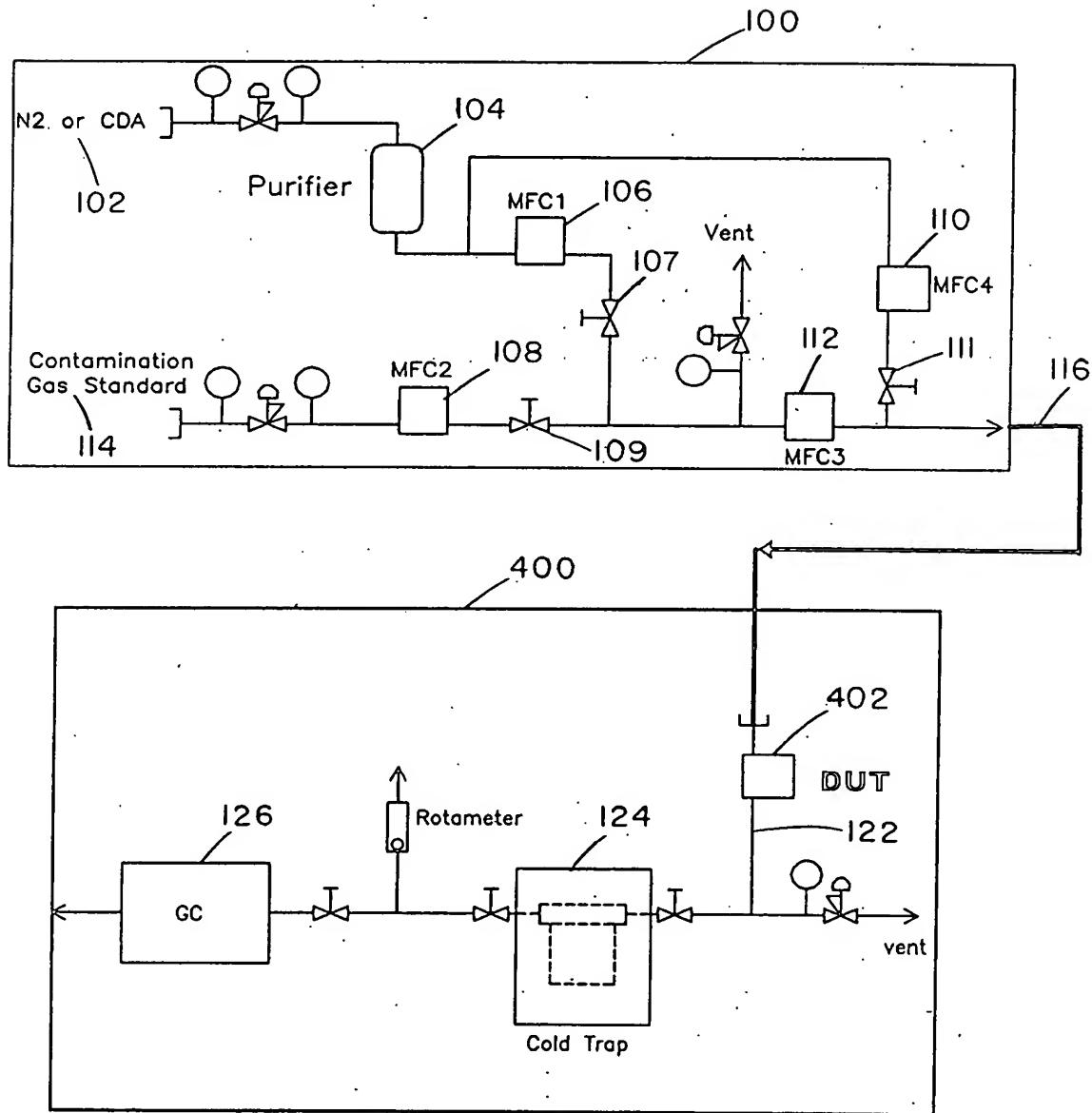
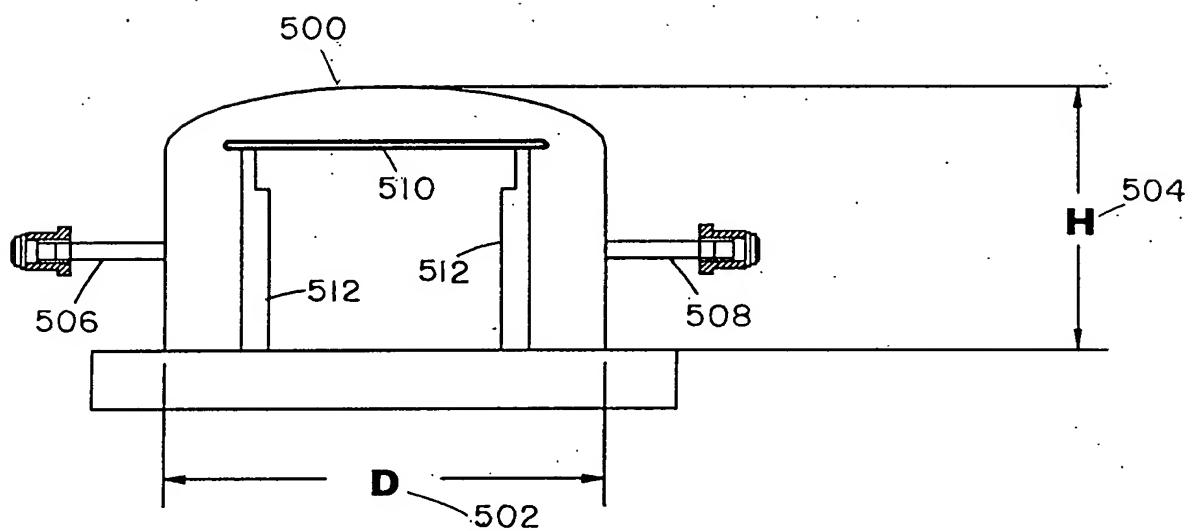


FIG. 4



Wafer Chamber

FIG. 5

5/19

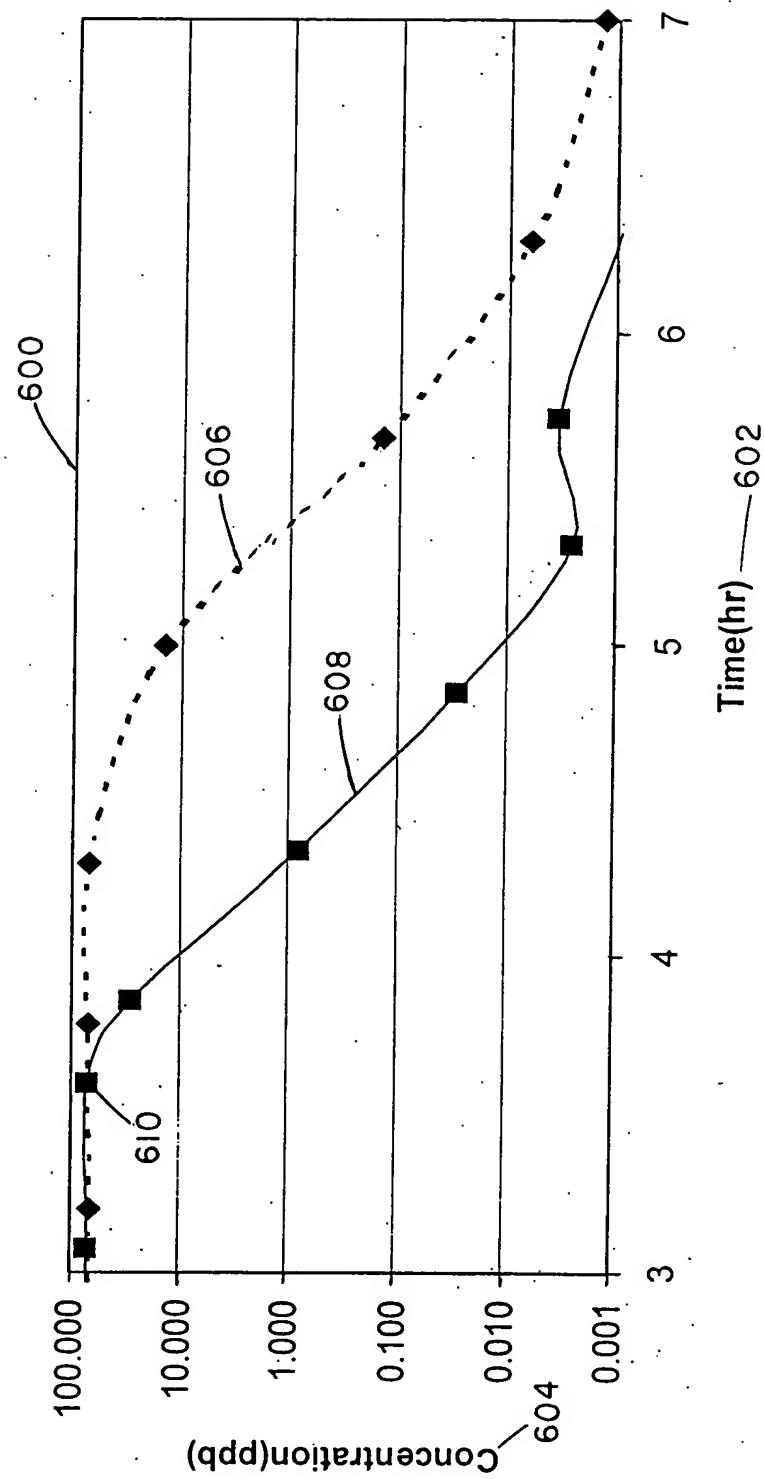


FIG. 6

6/19

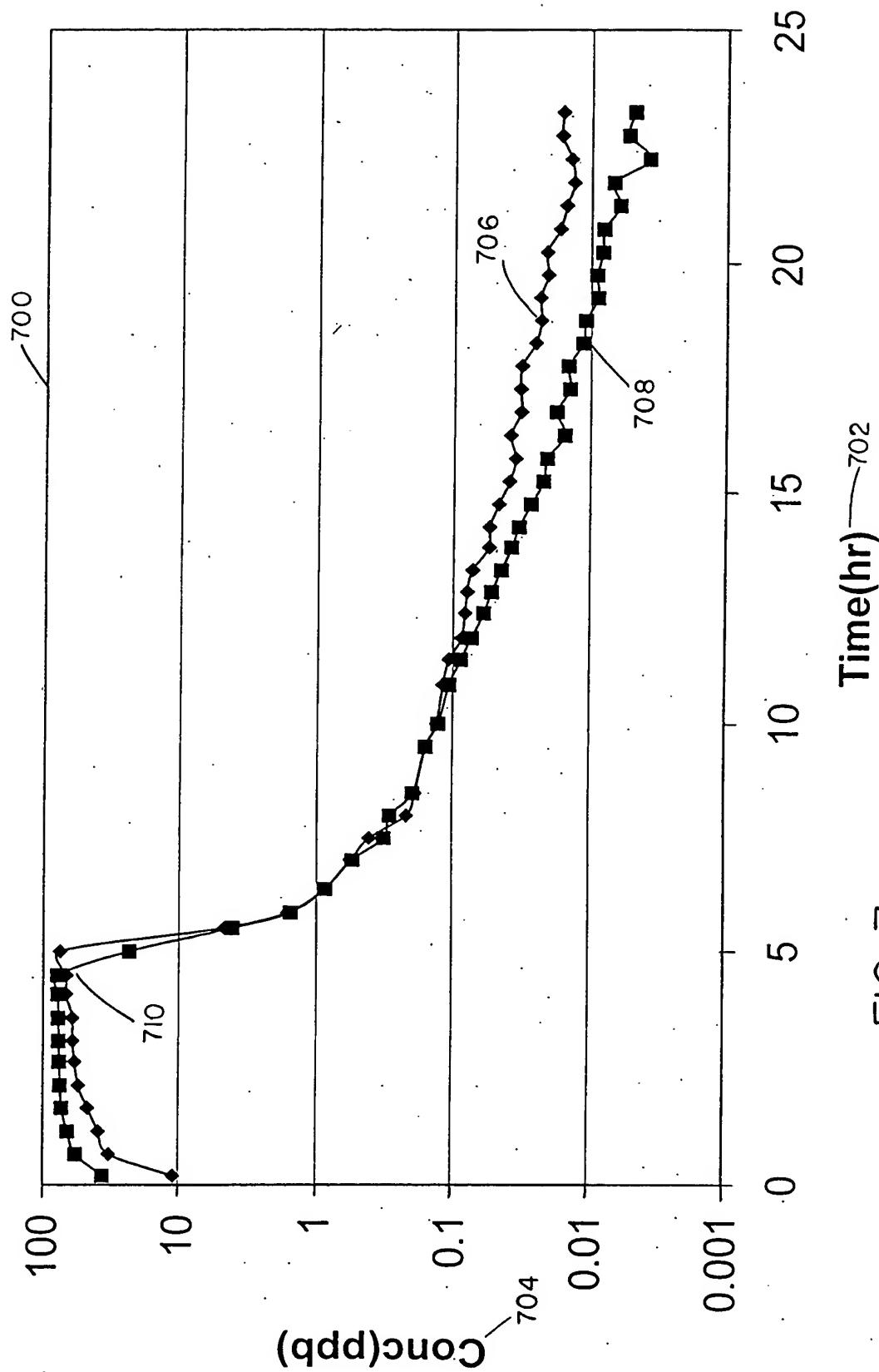


FIG. 7

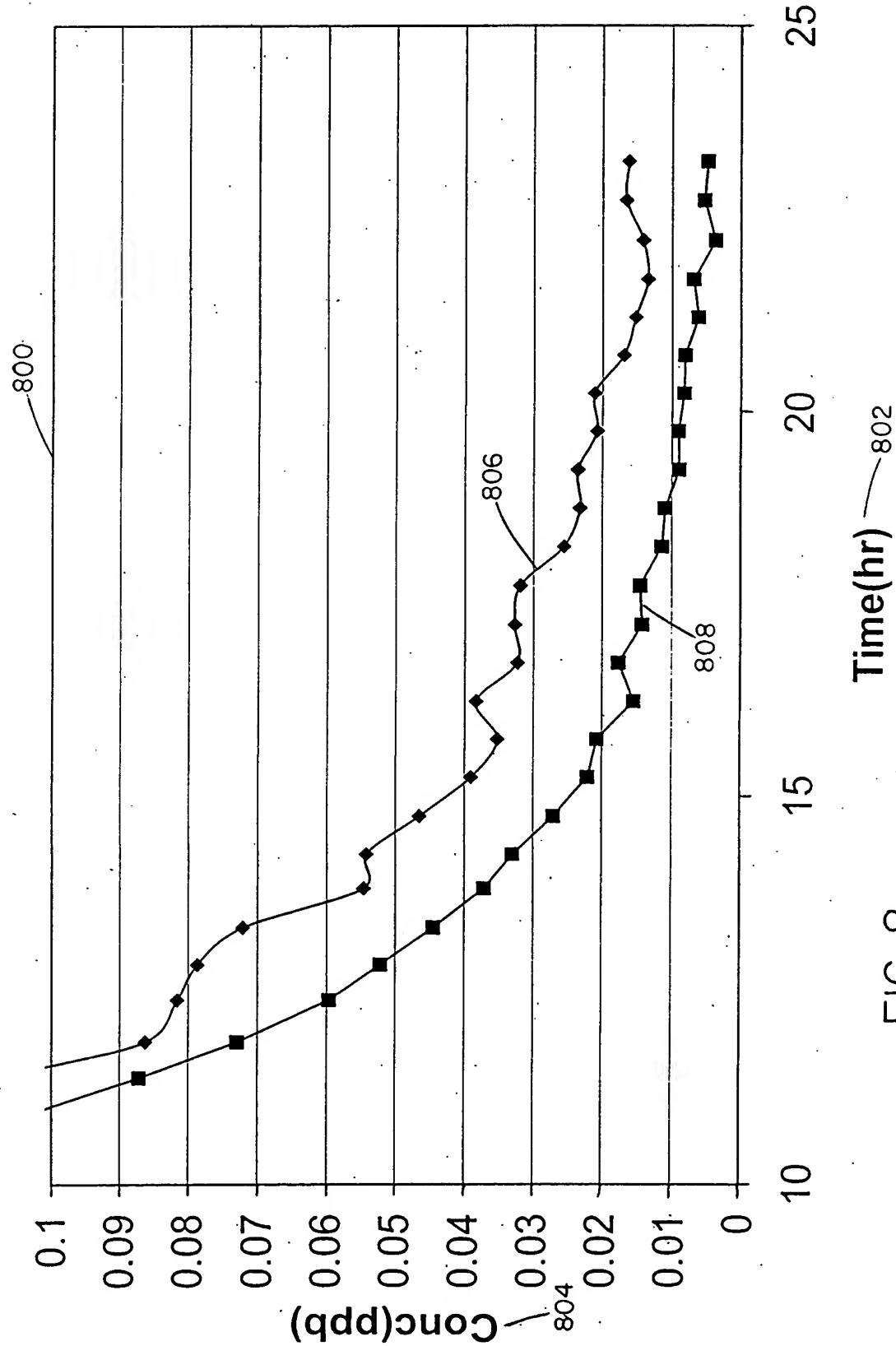


FIG. 8

8/19

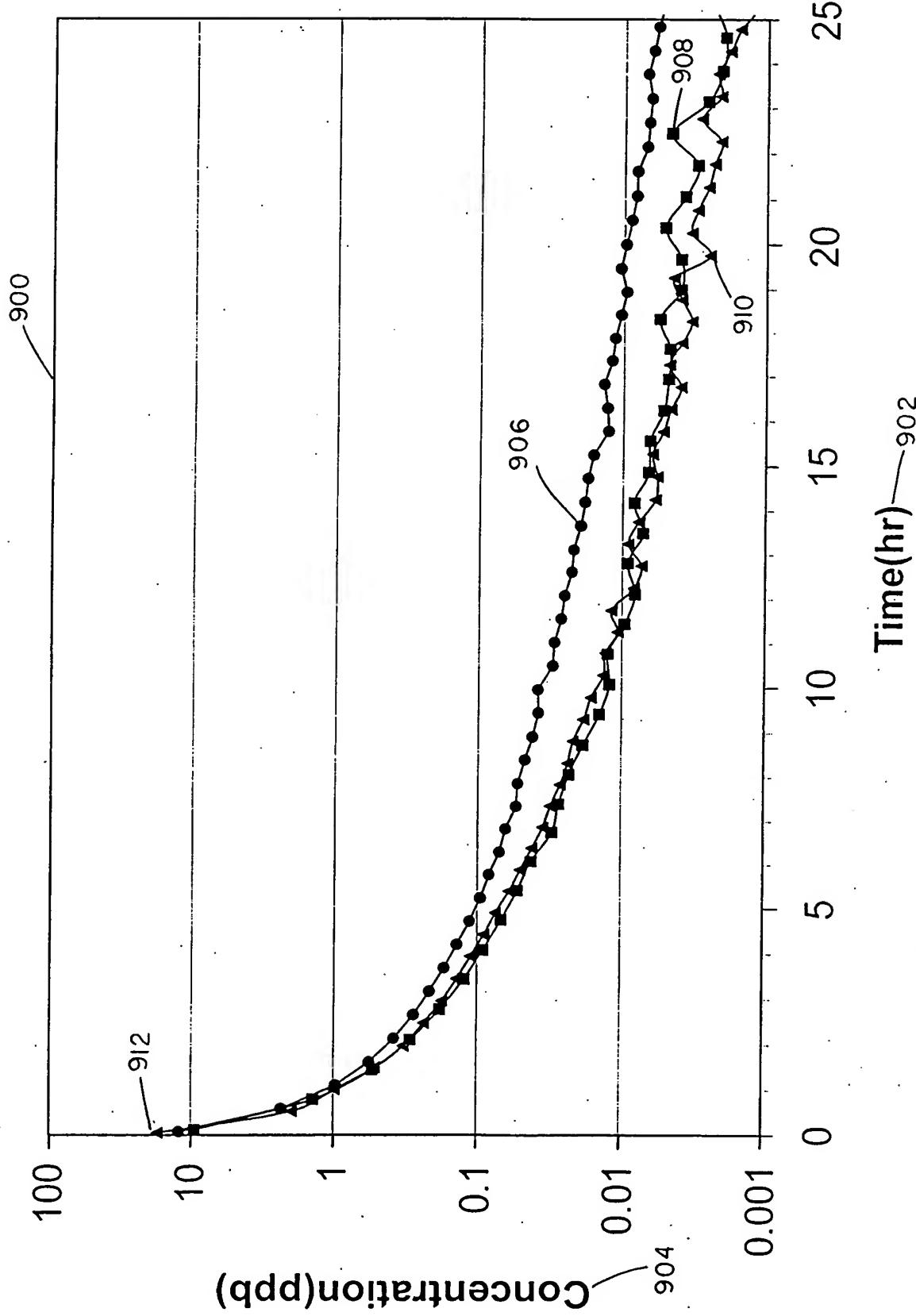


FIG. 9

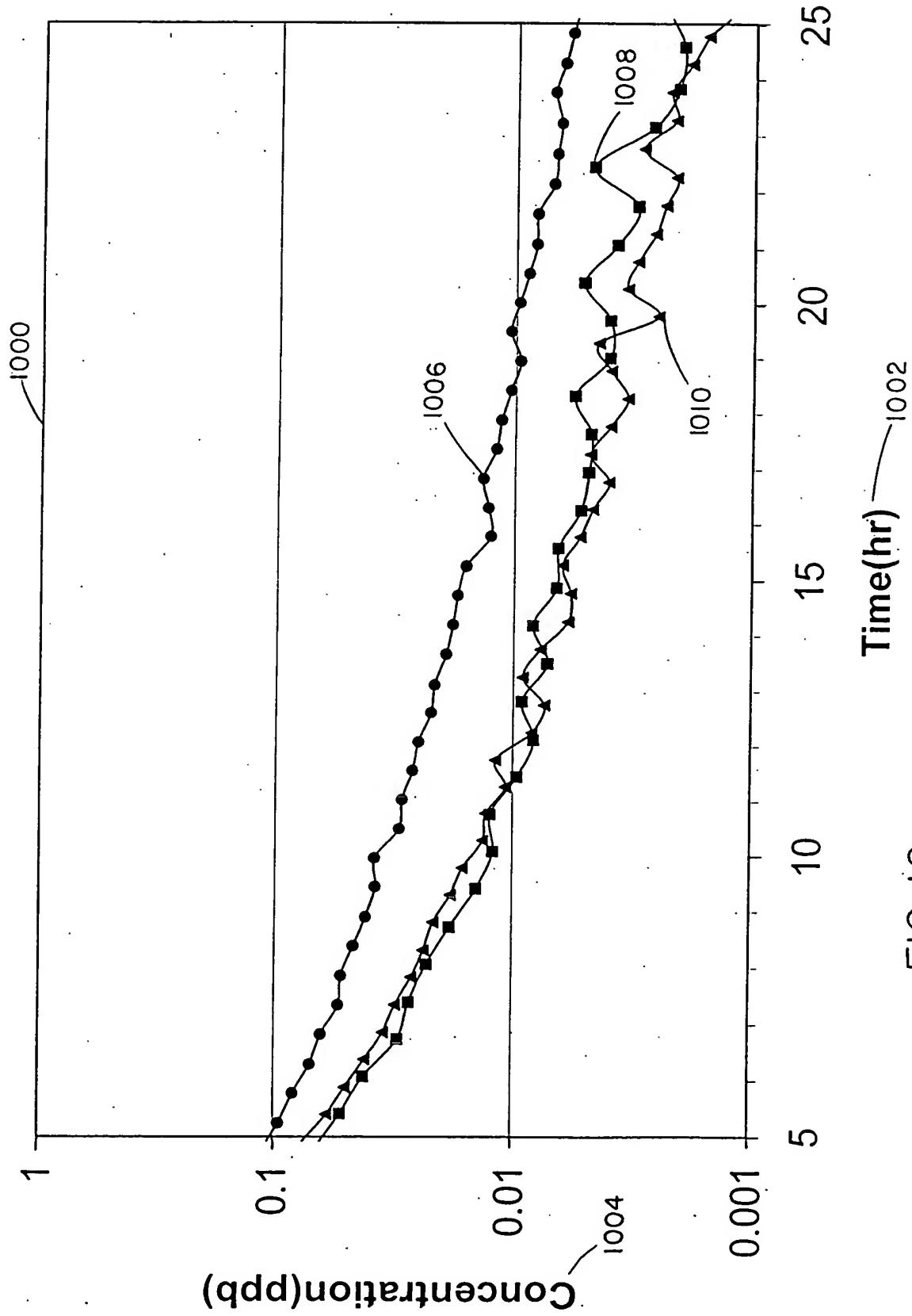


FIG. 10

10/19

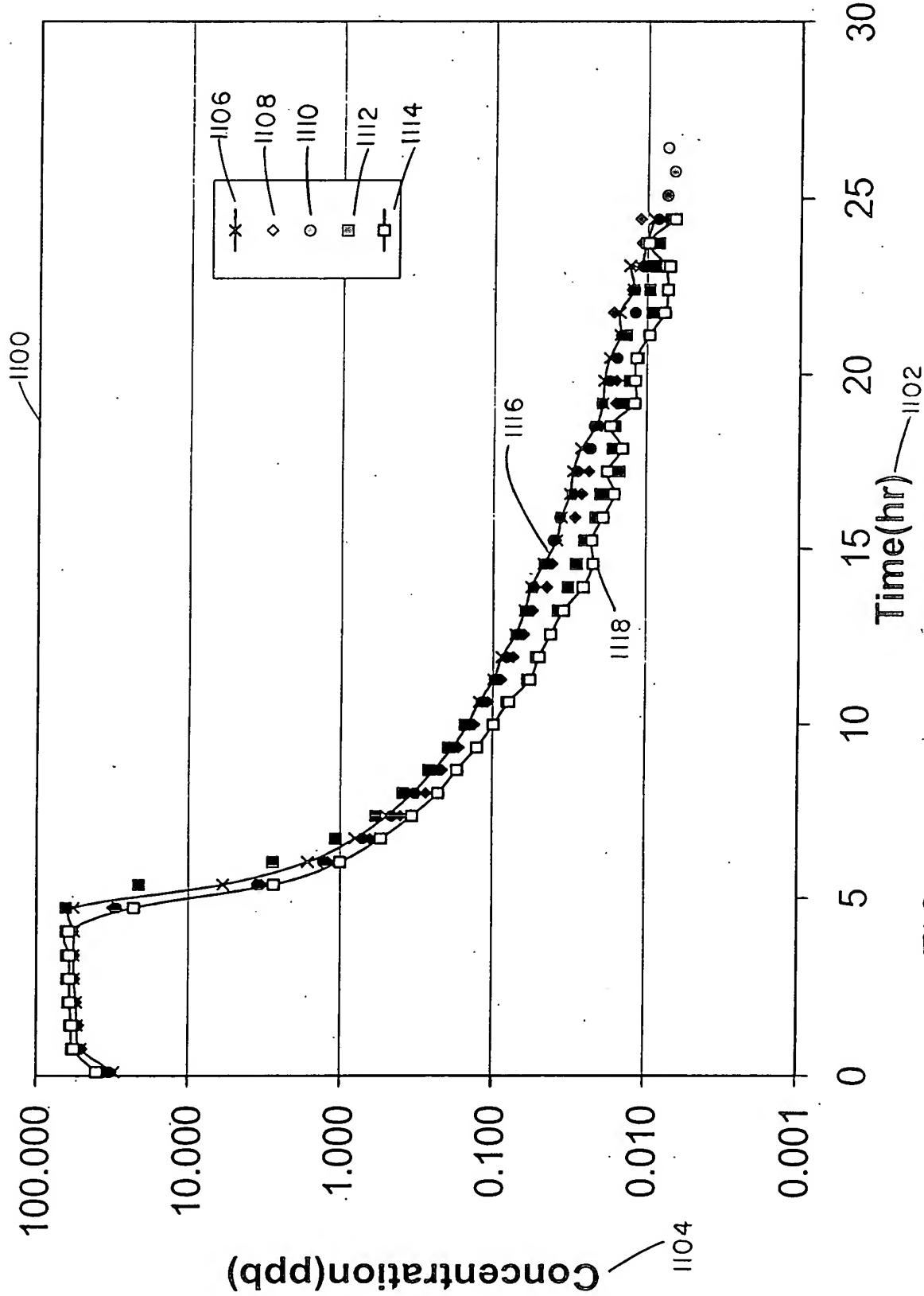
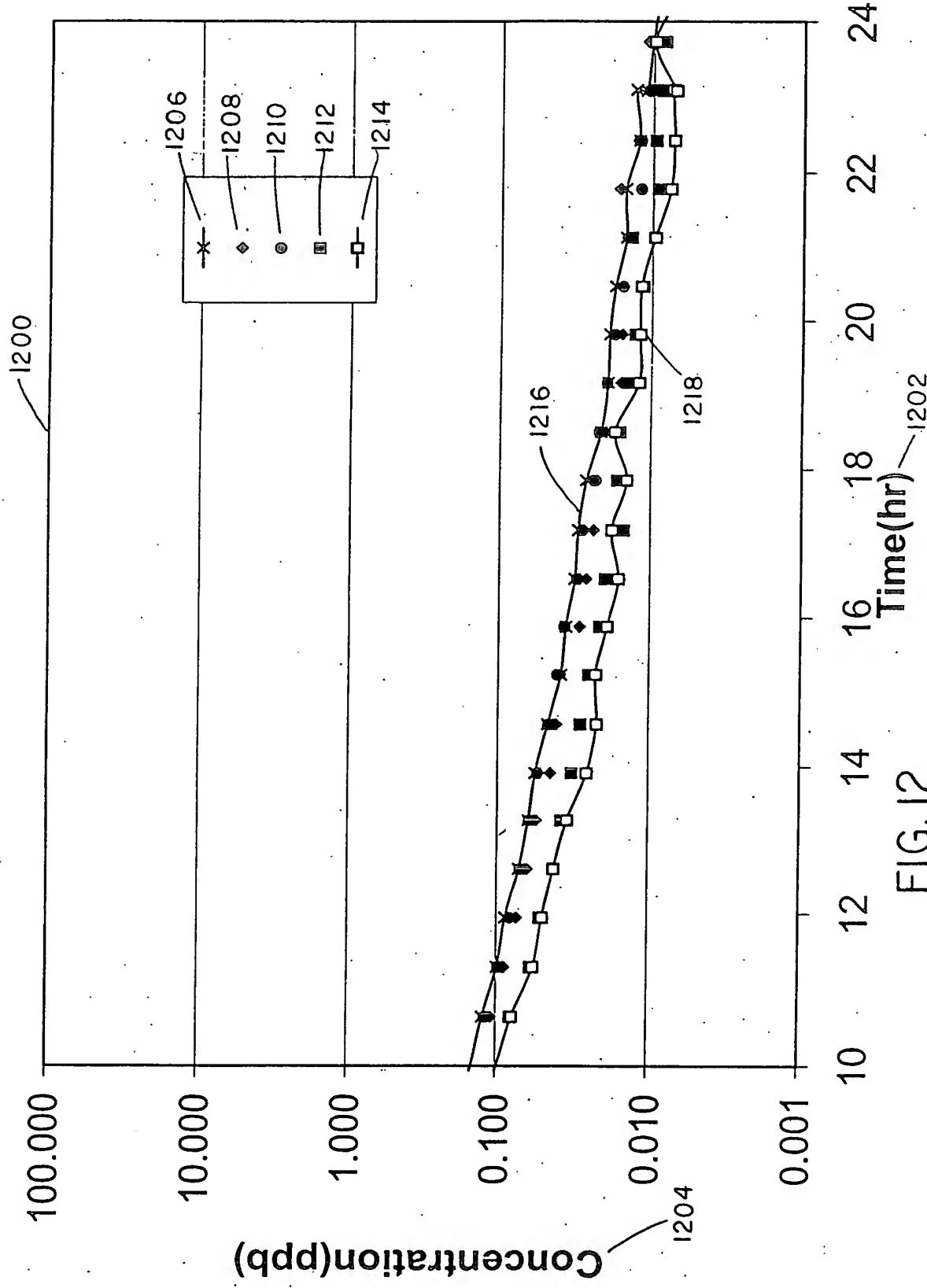


FIG. II

61/11



12/19

Figure 13

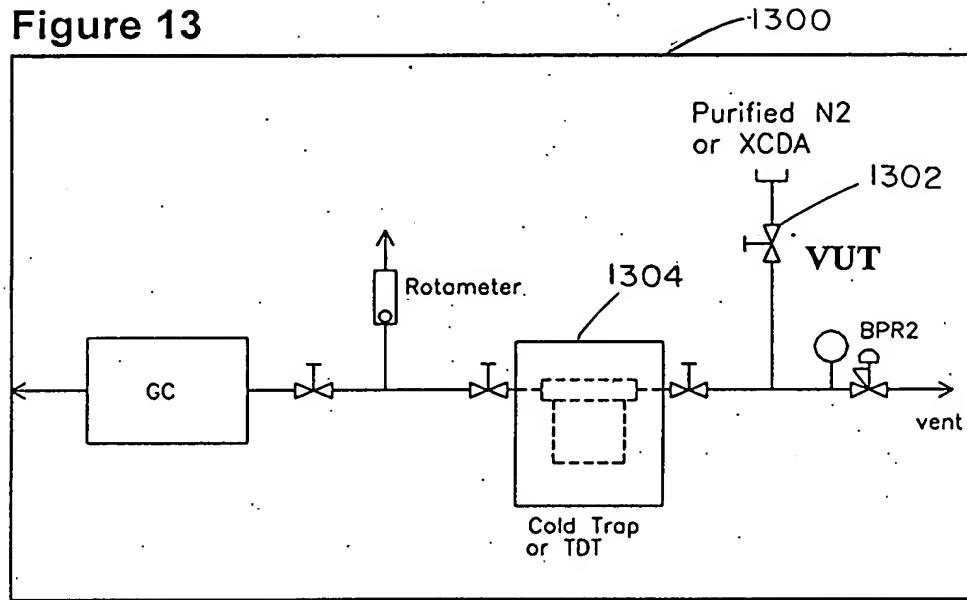
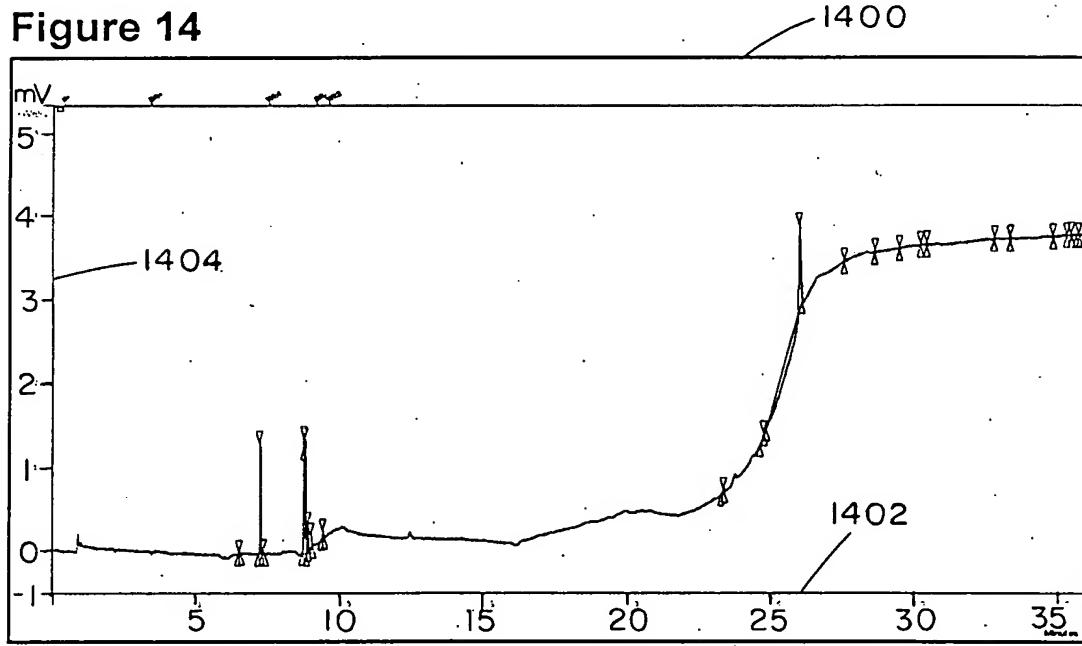
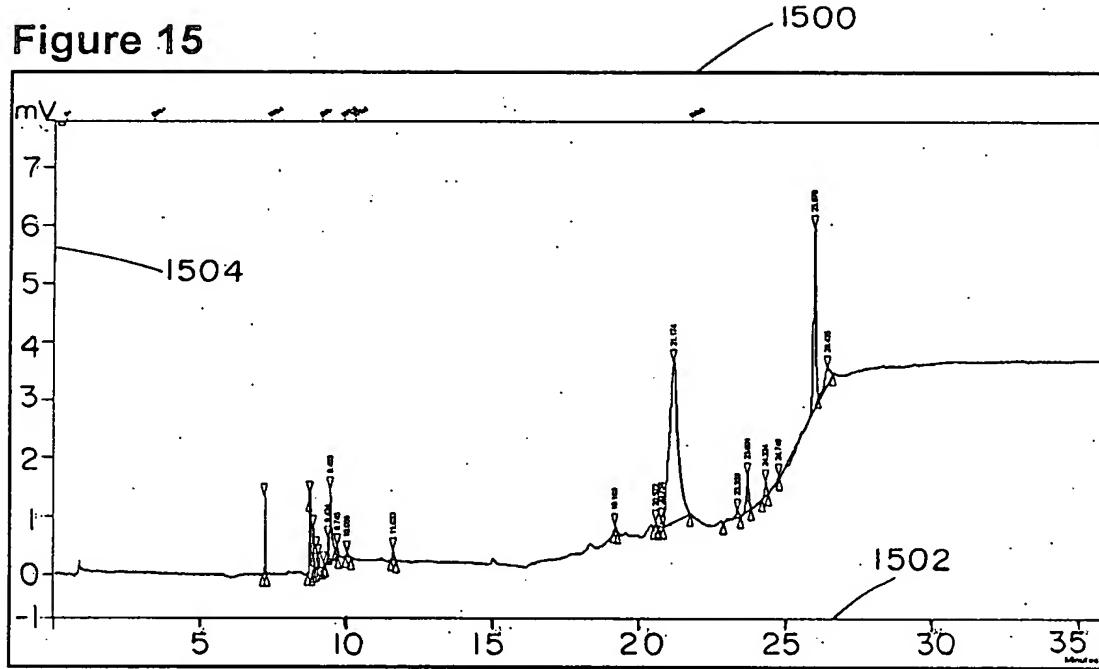


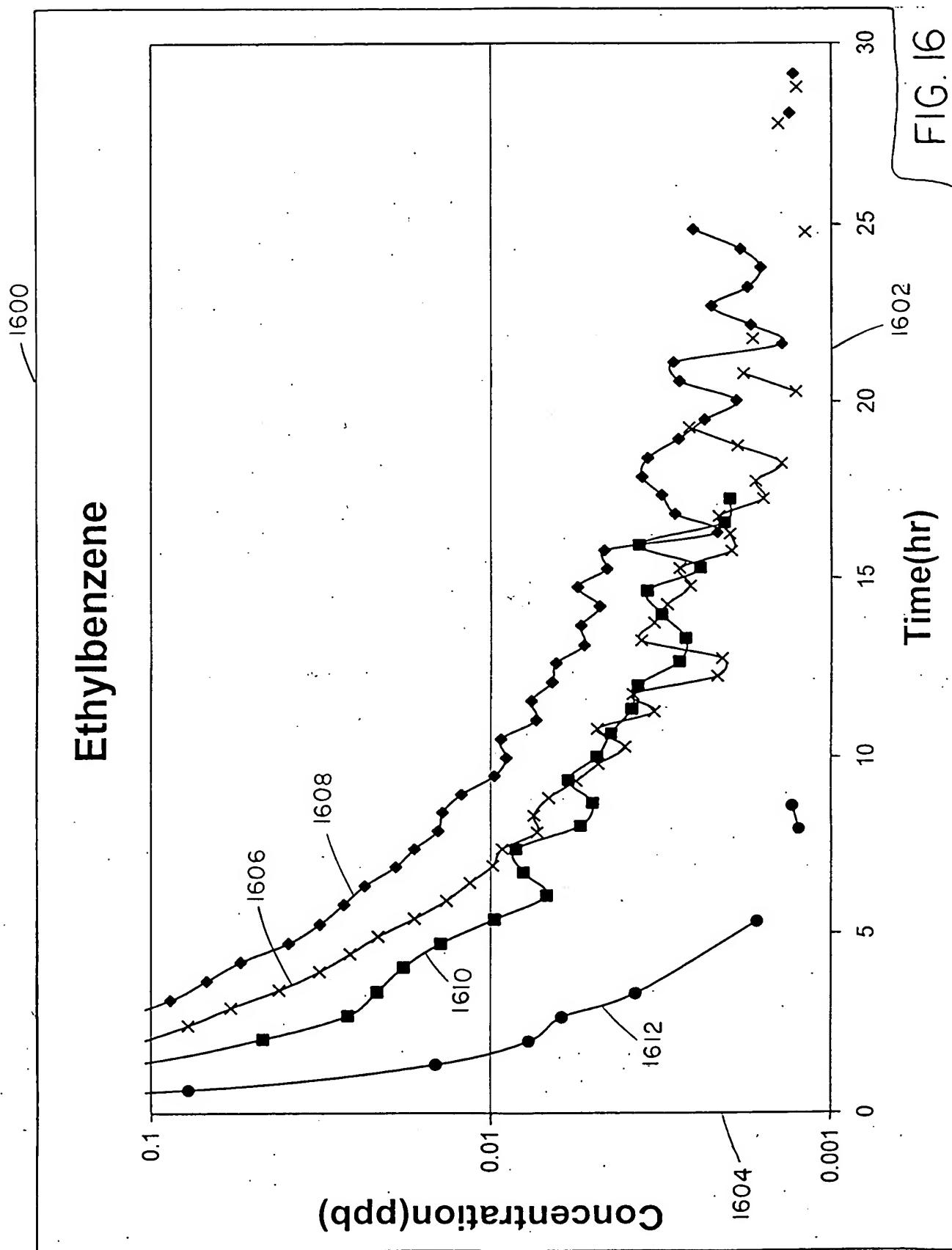
Figure 14



13/19

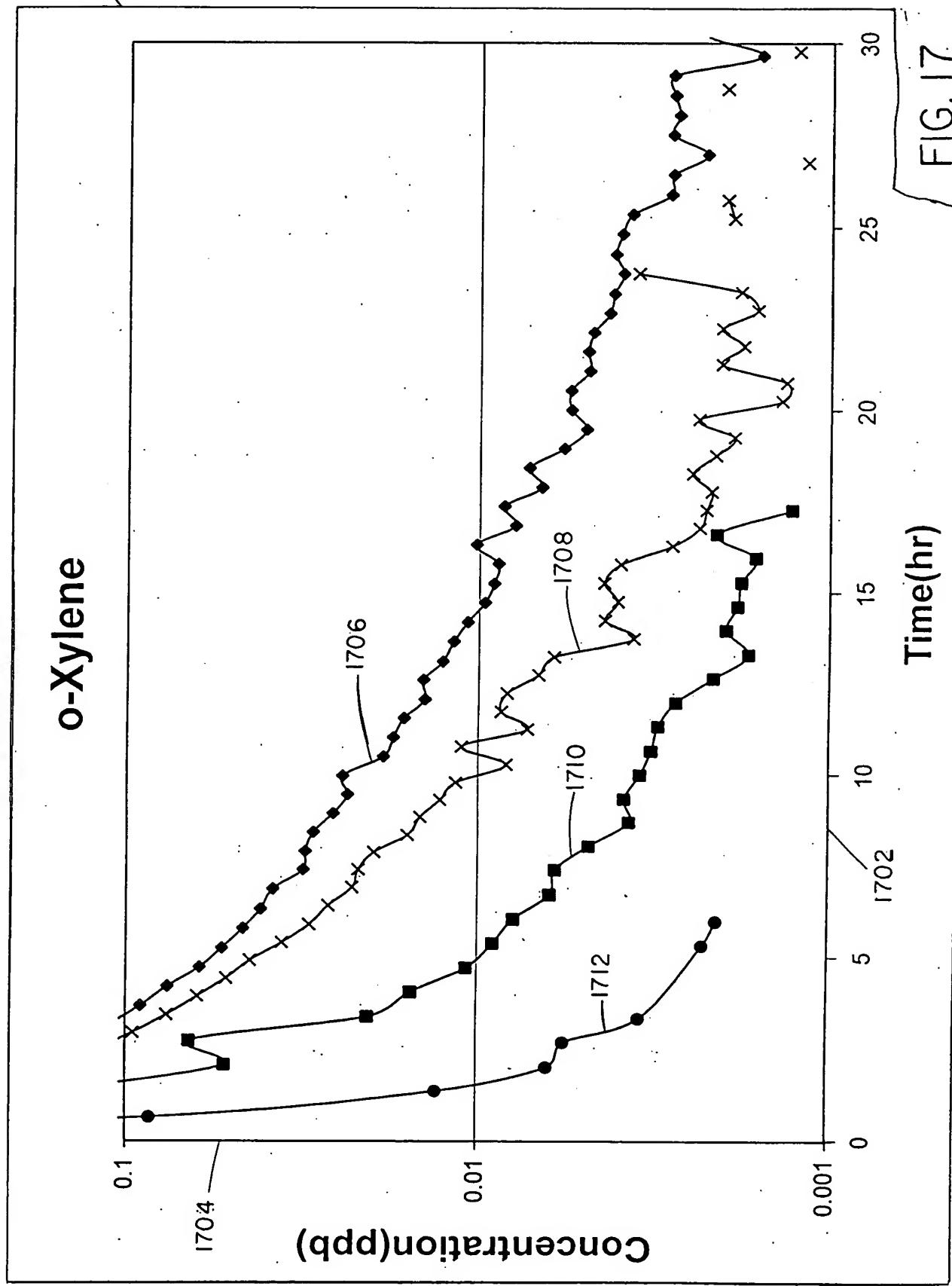
Figure 15

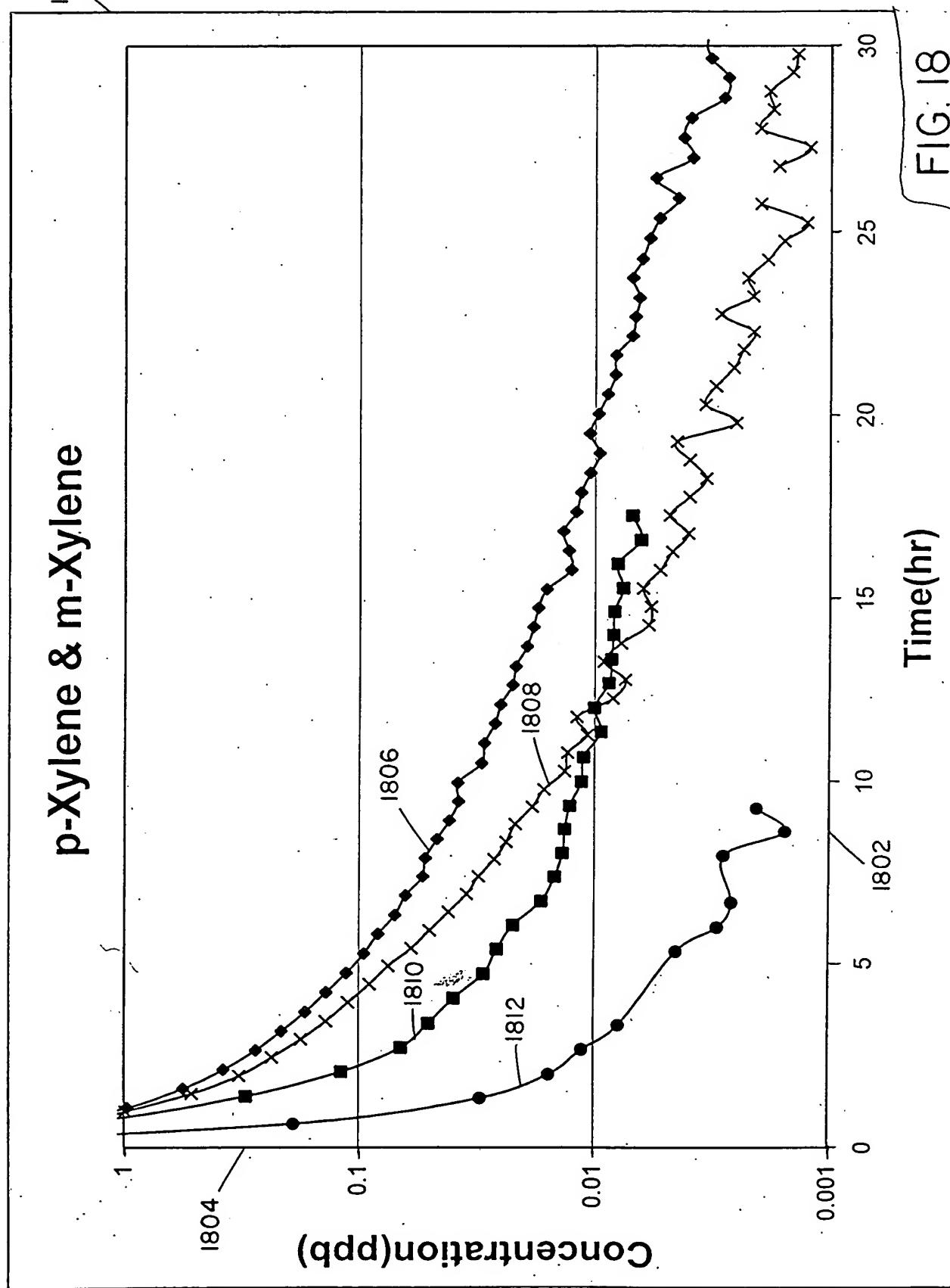




1700

15/19





18
FIG

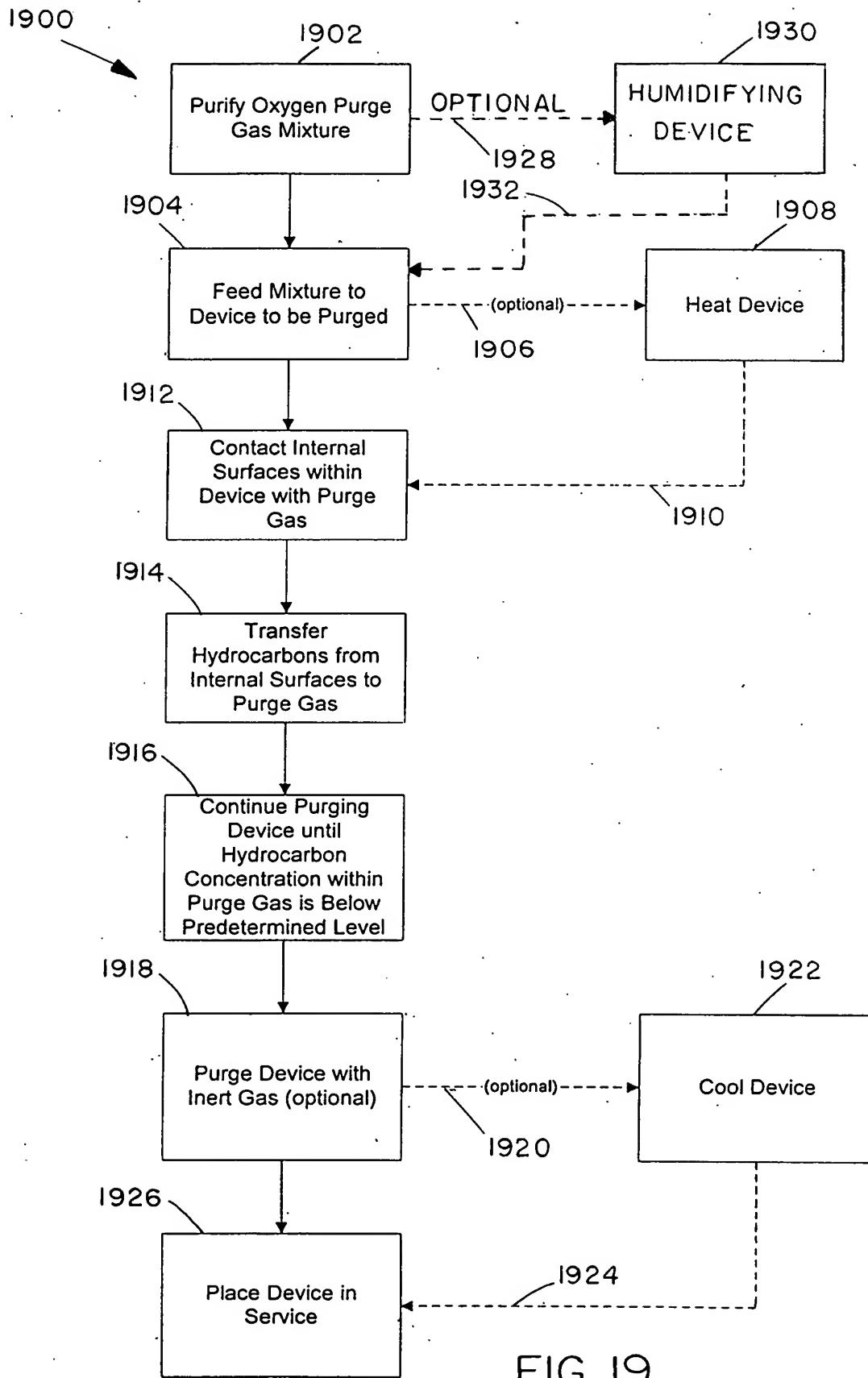


FIG. 19

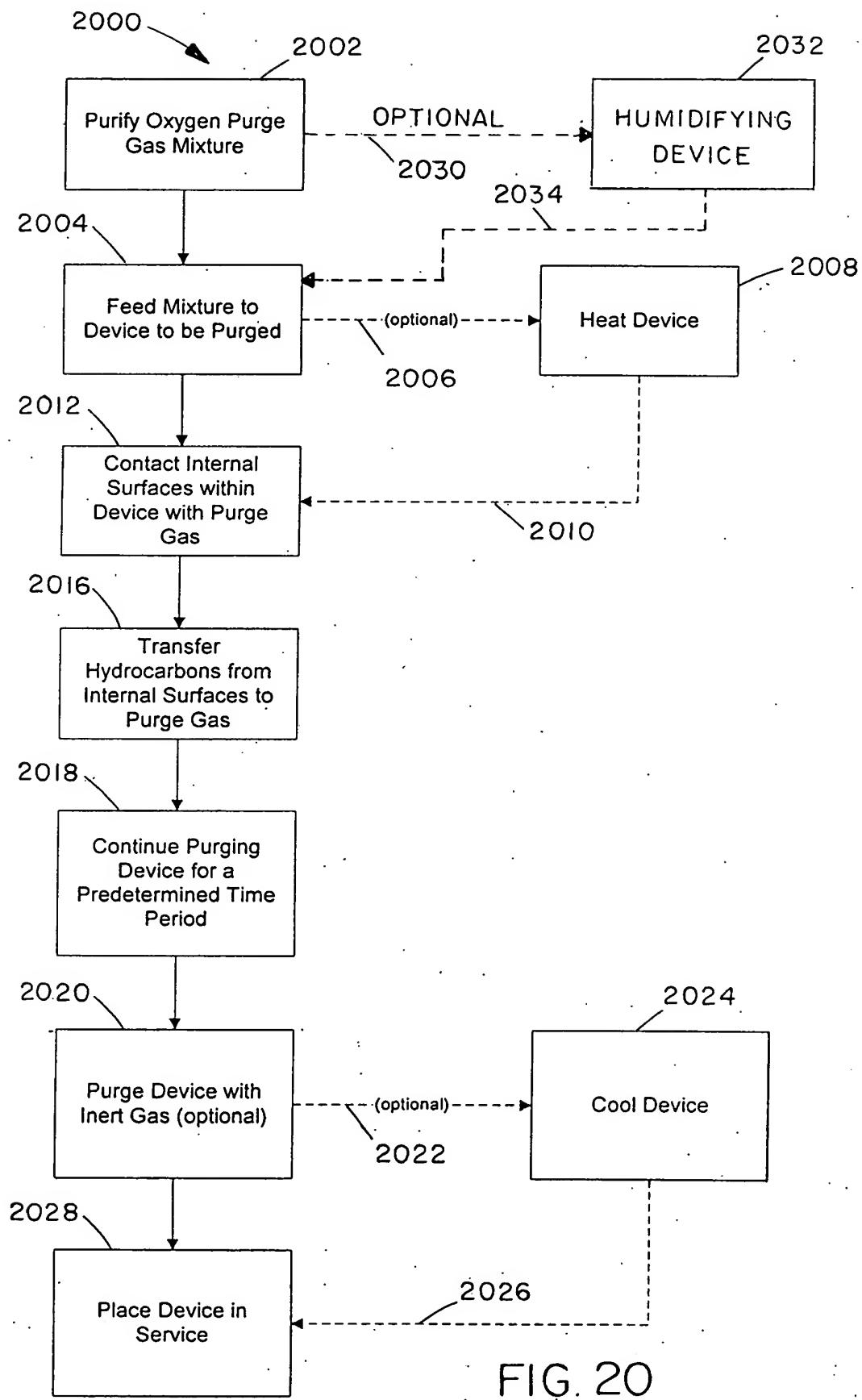


FIG. 20

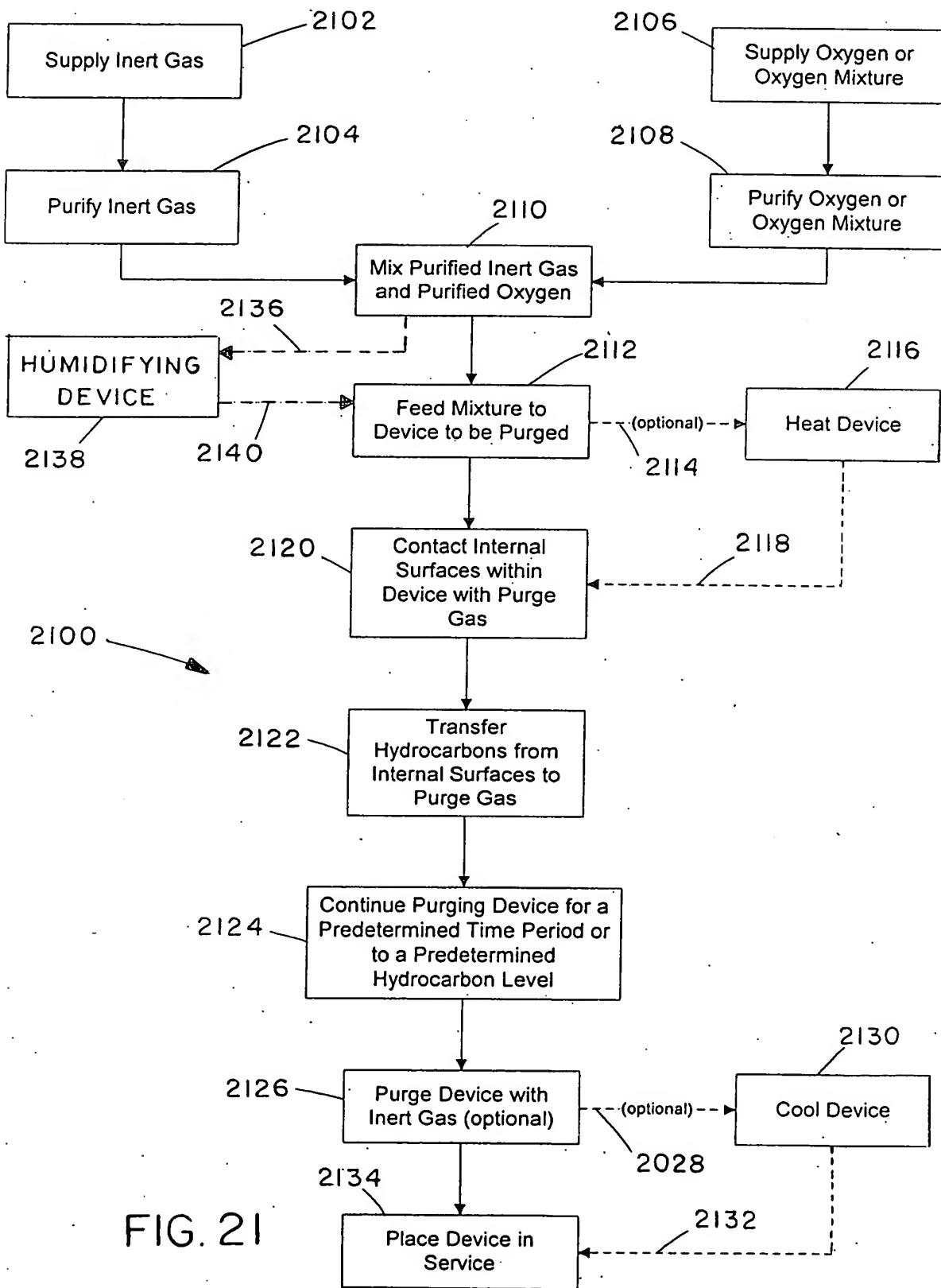


FIG. 21